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Sheet 1 of 1

| FORM 1449* INFORMATION DISCLOSURE STATEMENT IN AN APPLICATION (Use several sheets if necessary) | Docket Number: 10823.1744USWO | Application Number: 10/553,860 |
|---|---|---|
| | Applicant: NANAI et al. | |
| | Filing Date: October 20, 2005 | Group Art Unit: 2818 |

**U.S. PATENT DOCUMENTS**

| EXAMINER INITIAL | DOCUMENT NO. | DATE | NAME | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE |
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**FOREIGN PATENT DOCUMENTS**

| | DOCUMENT NO. | DATE | COUNTRY | CLASS | SUBCLASS | TRANSLATION | |
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| | | | | | | YES | NO |
| | JP 11-251259 | 9/17/1999 | Japan | | | Abstract | |
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**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)

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| | | Avouris et al. "Carbon Nanotube Transistors and Logic Circuits," PHYSICA B, October 2002, vol. 323, Issues 1-4, pp. 6-14 |
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.

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